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** CONTINUING DATA *****

This application is a DIV of 10/288,539 11/06/2002 PAT 6,660,597

** FOREIGN APPLICATIONS *****

JAPAN 2001-350636 11/15/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	17	23	3
Verified and Acknowledged	Examiner's Signature <i>m</i> Initials				

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TITLE

METHOD FOR MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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